

Dr. KHAJA DADA ATAR

M.Sc. (Electronics), Ph.D., SET, NET

Sana, Plot no. 38/2 G. M. Colony, New Balinga, Tal- Karveer, Dist- Kolhapur 416010

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OBJECTIVE:

Looking for a teaching position to offer higher dedication to student development and relevant educational needs.

Degree	Orgnization	Subject	Awarded
NET	University Grants Commission Delhi	Electronic Science	8 th July 2018
SET	Savitribai phule University of pune	Electronic Science	29 th May 2016
SET	Savitribai phule University of pune	Electronic Science	6 th September 2015

Degree	University	Subject	Awarded
Ph.D.	Shivaji University, Kolhapur	Exhaust Gas Recirculation Process Optimization-A Fuzzy Logic Simulation Approach	2016

Degree	Institute	University	Year	Achievement
M.Sc	Department of Electronics	Shivaji University, Kolhapur	2011	66.76
B.Sc	K.B.P. College Pandharpur	Solapur University, Solapur	2009	65.60
HSC	G. D. P. College Malshiras	Pune Board	2006	58.59
SSC	G. D. P. Malshiras	Pune Board	2004	55.86

TEACHING EXPERIENCE:

- Two year teaching experience at UG level in **The New College Kolhapur**.
- Six year teaching Experience at UG level in Rajershri Chhatrapati Shahu College Kolhapur under Rayat Shikshan Sanstha Satara.
- One year teaching Experience at M.Sc. Electronics in Shivaji University, Kolhapur.
- Worked as a visiting faculty of B. Tech computer in Shivaji University Kolhapur.
- Served as committee Member for effective competition “RESONANCE” for robots and Embedded Systems held in Rajaram college-2014.
- Organized remarkable National Electronics conference “LATEST-2014” Held at Shivaji University Kolhapur, as its organizing body constituent.
- hand out as committee member for Five Days workshop on “Architecture Programming Application of microcontroller-8051” held on Department of Electronics Shivaji University Kolhapur
- Guided to more than 100 student of final year annual project for ME, M. Sc., B.Sc. and BE

INVITED TALKS AND GUEST LECTURES:

- Invited for Technical talk on “Introduction and simulation of electronic circuits” for B.Sc. students, Organized by D. R. Mane college Kagal.
- Motivated M.Sc. Electronics students in Invited talk on “Fuzzy Logic and its application” Organized by Rashtrasant Tukadoji Maharaj Nagpur University at Nagpur.
- Invited for Technical talk on “Advanced microcontroller (PIC) and Embedded programming” for B.Sc. Electronics, Organized by Smt. K. W. C. College Sangli.
- Presented a Guest lecture on communication system in workshop for M.Sc. Electronics organized by UGC at Department of Electronics, Shivaji University.
- Delivered guest Lecture on “Power Electronics” for SC/ST and Minority student workshop on M.Sc. Electronics Organized by Department of Electronics Shivaji University Kolhapur.

TECHNICAL SKILLS/ ACCOMPLISHMENTS:

- Excellent in 8085 & Intel 86 family processor knowledge and there assembly programming and architecture.
- Magnificent in C language and its programming with rational logic.
- Efficacious in Basic Electronics, Fundamentals of Digital Electronics education.
- Well good in 8051 family, AVR microcontroller, PIC microcontrollers, MSP430 microcontrollers and there programming and interfacing.
- Skilled hands on PCB design and layout generation in EAGLE and Protel.
- Expert in technical computing language and scientific software tool “MATLAB and LABVIEW” in modeling, programming, GUI designing.
- Cognition in programming tools and integrated development tools like Keil and Pinacle for 8051, MPLAB and HI-TECH C, Micro C for PIC, CCS for MSP430 and VHDL.

BOOK PUBLICATION:

- “Application of Fundamental Electronics and Integrated circuits” LAP LAMBERT academic Publishing, Germany, ISBN- 978-620-4-20854-1

PAPER PUBLICATIONS:

- International Journal Publications = 16
- International Journal Publications (applied) = 03
- National Journal Publications = 02
- Full Paper proceeding Publications = 04
- Abstract proceeding Publications =26

ATTENDED CONFERENCE /WORKSHOP/SYMPOSIUM/SEMINAR:

- International Conference = 04
- National Conference /workshops= 13
- State Level Conference/Seminar /workshop= 04

AWARDS:

- Competitor of State level innovative scientific project competition “AVISKAR” arrange at Jalgoan.
- Awarded second Prize in AVISKAR at University level in Research category and selected for State level for Fuzzy Logic base automatic intelligent green house parameter control.
- Awarded First Prize in AVISKAR at distinct level in Research category and selected for University level for Fuzzy Logic base automatic intelligent green house parameter control.
- Third best paper presentation award on state level seminar at K. B. P. college Pandharpur
- Participated on Texas Instruments Innovation Challenge India Design Contest 2015
- Participated in Model computation on National Science Day 2016

PERSONAL INFORMATION:

- Name : Dr. Atar Khaja Dada
- Marital Status : Married
- Sex : Male
- Cast : Muslim Tamboli
- Languages known : English, Hindi & Marathi.
- Permanent Address : A/P- Sarkoli, Tal- Pandharpur, Dist-Solapur
- Date of Birth : 09th Jun 1988

WHOM TO REFER:

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| 1) Dr. R.R.Mudholkar.
Professor,
Department of Electronics,
Shivaji University,
Kolhapur.
Mob No. 9637774815
Email: rrm_eln@unishivaji.ac.in | 2) Prof. (Dr.) R. K. Kamat
Head,
Department of Electronics,
Shivaji University,
Kolhapur |
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I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

Yours Sincerely

(Dr. Khaja Dada Atar)